

## ABSTRACT OF THE DISCLOSURE

A clock generator circuit, comprising: a multi-phase clock signal generator for generating a plurality of clock signals having a same frequency but difference phases according to a reference clock signal; a modulation  
5 device for generating a phase modulation signal through Delta-Sigma modulation; and a phase modulator, which is electrically coupled to the modulation device, for selecting one of the clock signals to be a modulated clock signal according to the phase modulation signal.

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